

7,035,330 B2

PATENT

11471.00.0017

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Shanbhag et al.)
U.S. Patent No.: 7,035,330)
Issued: April 25, 2006)
For: DECISION FEEDBACK)
EQUALIZER WITH DYNAMIC)
FEEDBACK CONTROL)

**REQUEST FOR CERTIFICATE OF
CORRECTION OF PATENT FOR
PTO MISTAKE 37 CFR § 1.322(a)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Attn: Certificate of Correction Branch

VIA ELECTRONIC

Sir:

Pursuant to 37 C.F.R. §1.322(a), hereby request that a Certificate of
Correction be issued for U.S. Patent No. 7,035,330 B2.

Attached is Form PTO/SB/44 with errors and changes requested.

The exact location where the errors occur in the Certificate of Correction is:

Column 2, lines 38-64

Column 3, lines 4, 11, 18, 20

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Please send the corrected Certificate to:


Mark A. Dalla Valle, Esq.
Vedder, Price, Kaufman & Kammholz, P.C.
222 North LaSalle Street, 24th Floor
Chicago, IL 60601

It is believed there is no fee due. In the event there is a fee, the Commissioner is hereby authorized to charge any additional fees to Deposit Account Number 22-0259.

Respectfully submitted,

VEDDER, PRICE, KAUFMAN & KAMMHOLZ, P.C.

Date: Oct 11, 2006

By: 
Mark A. Dalla Valle
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Atty. Docket: 11471.00.0017

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,035,330 B2
APPLICATION NO.: 10/768,408
ISSUE DATE : April 25, 2006
INVENTOR(S) : Shanbhag et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2, delete lines 38-64, and insert

--In accordance with one embodiment of the presently claimed invention, a decision feedback equalizer with dynamic feedback control for adaptively controlling a pre-slicer data signal that is sliced to provide a post-slicer data signal includes signal combining circuitry, signal slicing circuitry, decision feedback circuitry and signal differentiation circuitry. First signal combining circuitry combines a feedback signal and an input signal representing a plurality of data to provide a pre-slicer signal. The signal slicing circuitry is coupled to the first signal combining circuitry and slices the pre-slicer signal to produce a post-slicer signal indicative of the plurality of data. The decision feedback circuitry includes input signal timing control, is coupled to the signal slicing circuitry, and feeds back the post-slicer signal in response to a control signal to produce the feedback signal. Second signal combining circuitry is coupled to the signal slicing circuitry and combines the pre-slicer and post-slicer signals to produce a difference signal indicative of a difference between the pre-slicer and post-slicer signals. The signal differentiation circuitry includes a selected signal delay and differentiates and delays the input signal to produce a resultant signal, wherein respective portions of the differentiated signal are delayed relative to corresponding portions of the input signal by the selected signal delay. Third signal combining circuitry is coupled to the second signal combining circuitry and the signal differentiation circuitry, and combines the difference signal and the resultant signal to produce the control signal, wherein the selected signal delay is selected such that the control signal has a substantially zero AC signal component.--

Column 3:

Line 4, delete "First" and insert --A first--

Line 11, delete "Second" and insert --A second--

Line 18, delete "differentiated" and insert --resultant--

Line 20, delete "Third" and insert --A third--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Mark A. Dalla Valle, Vedder, Price, Kaufman & Kammholz, P.C.
222 N. LaSalle St.
Chicago, IL 60601

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**